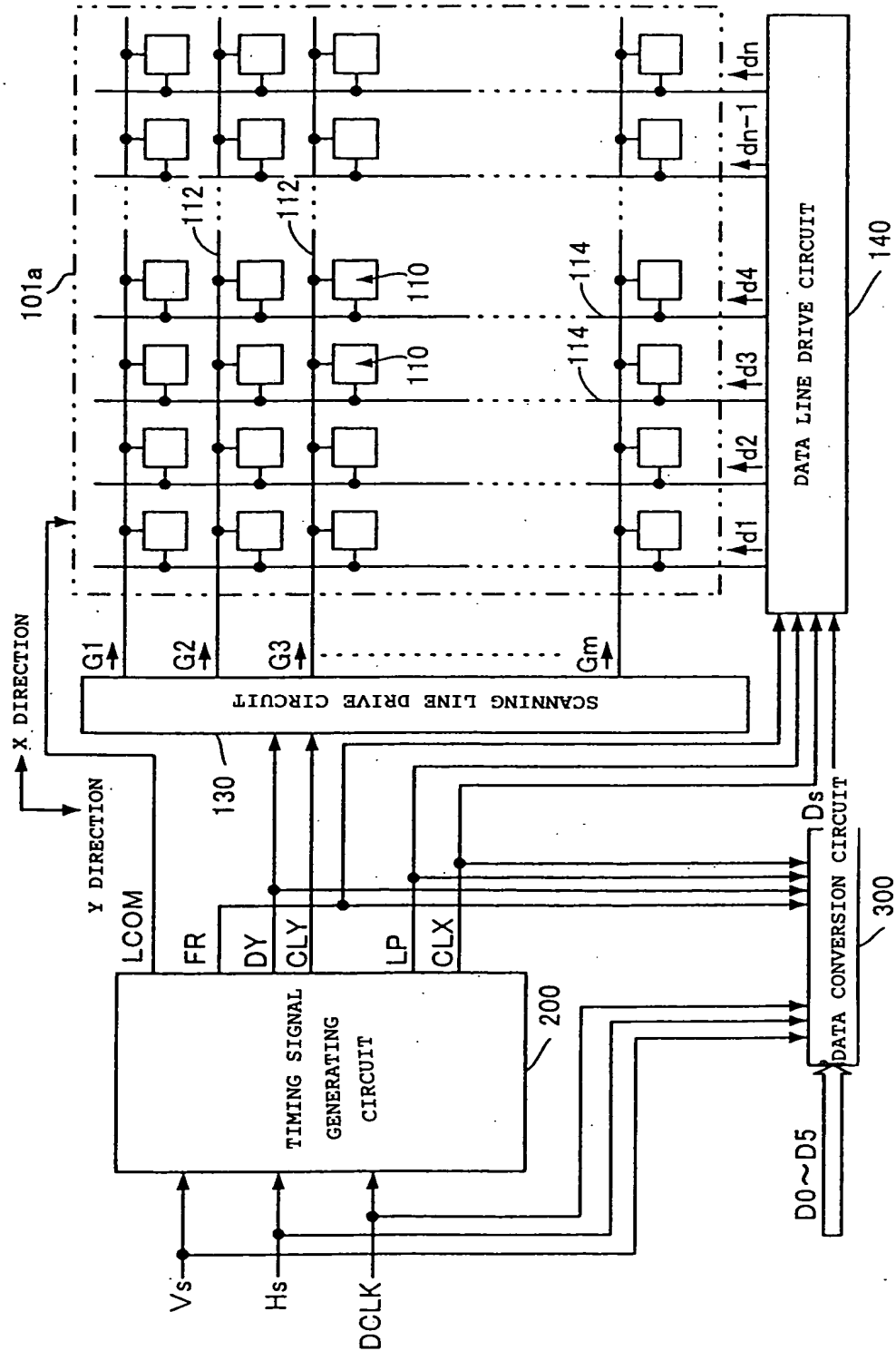
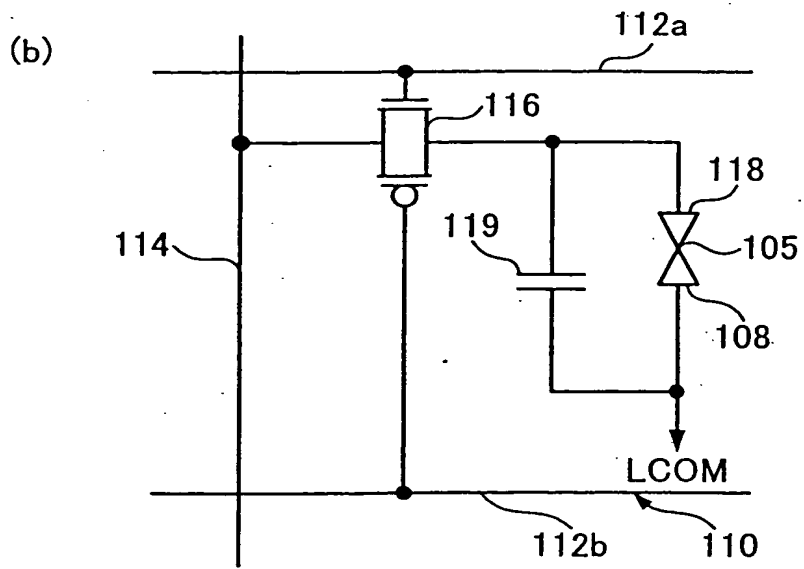
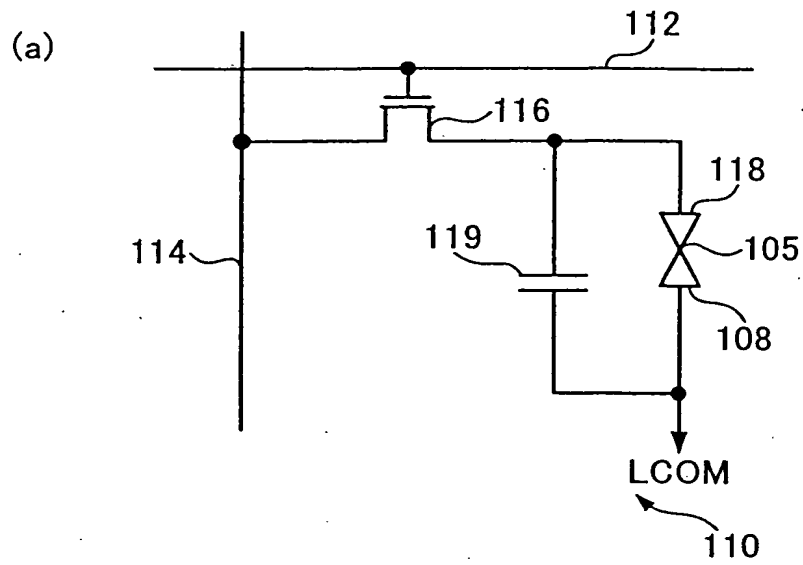


[NAME OF DOCUMENT] DRAWINGS

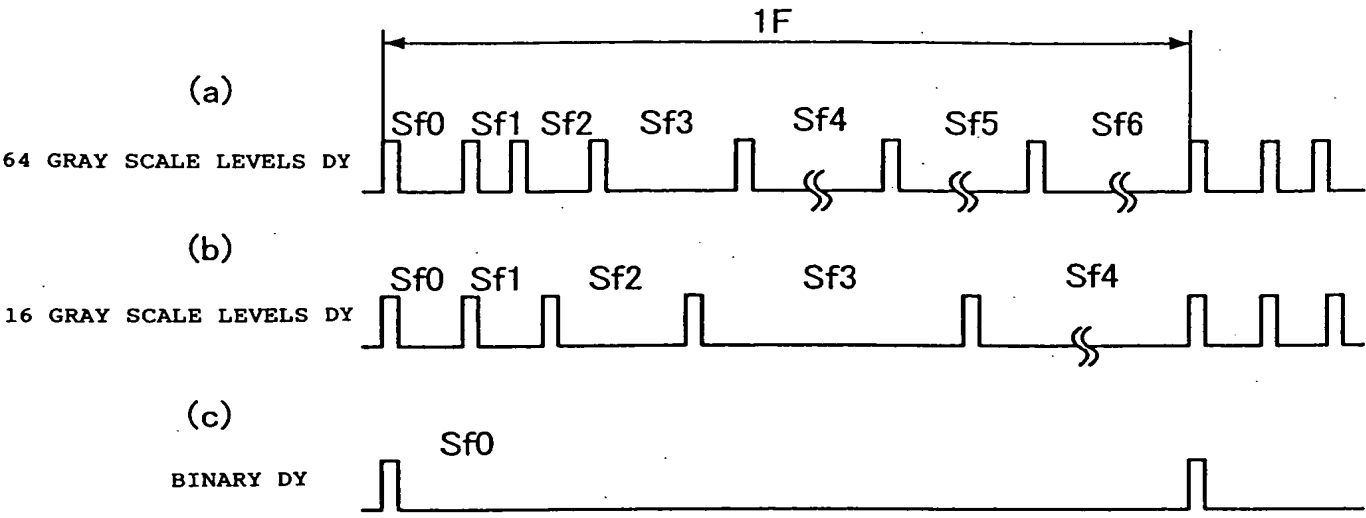
[FIG. 1]



[FIG. 2]



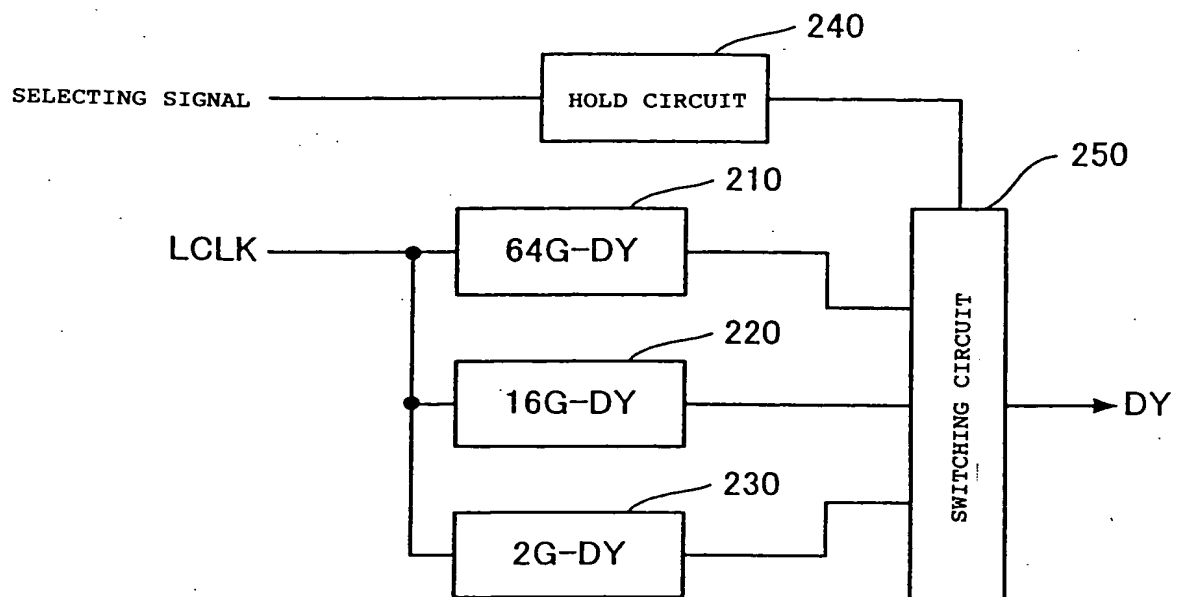
[FIG. 3]



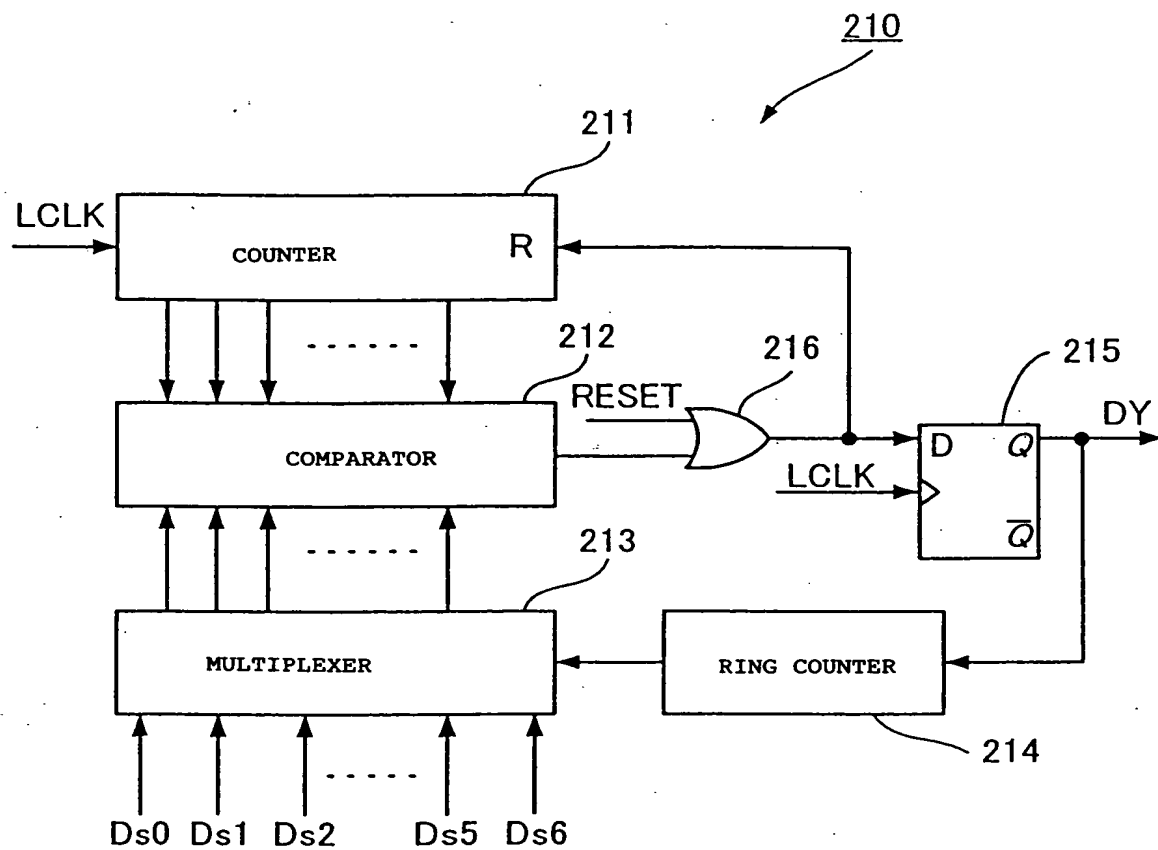
TOP SECRET

[FIG. 4]

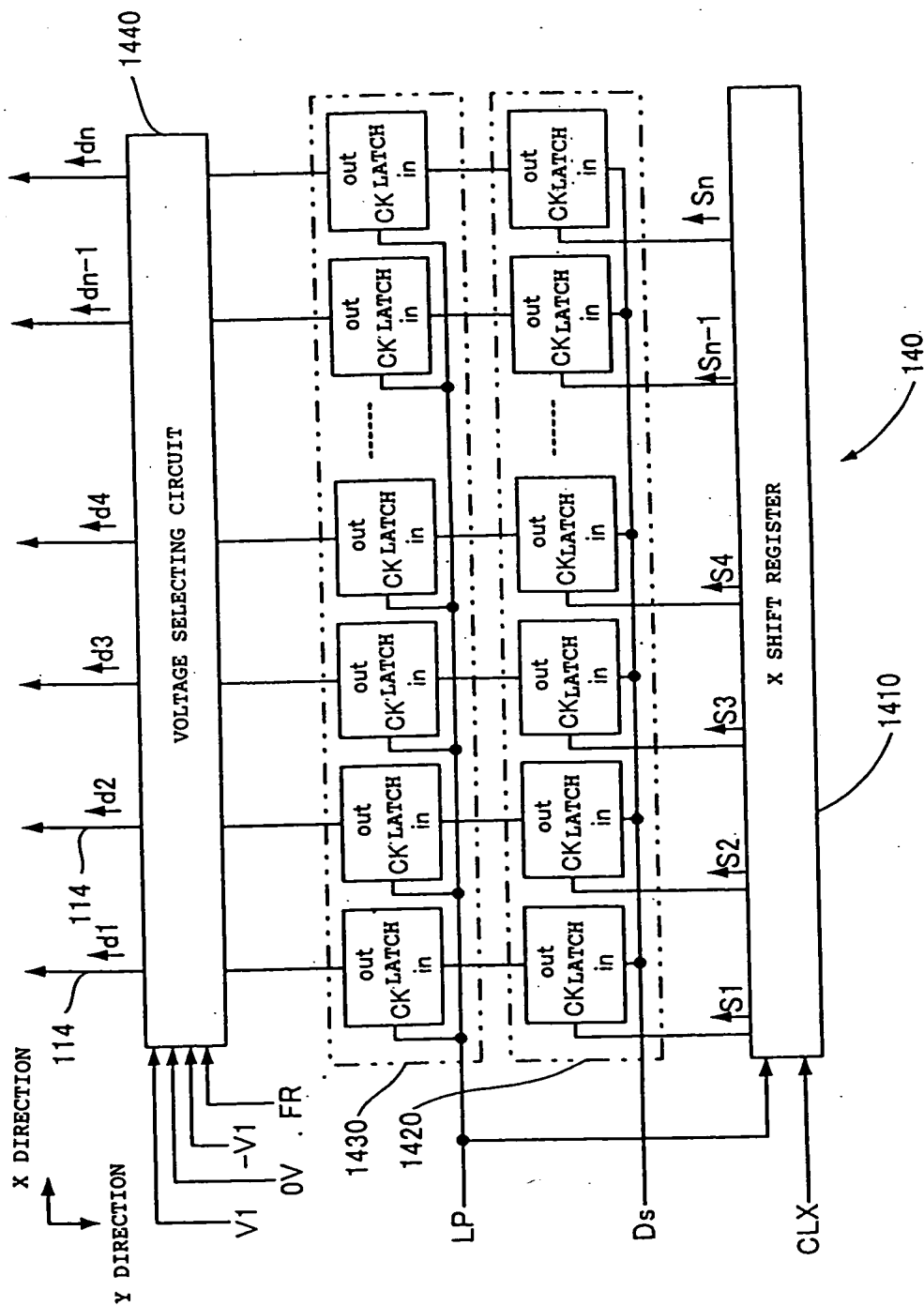
DY SELECTING CIRCUIT



[FIG. 5]



[FIG. 6]



[FIG. 7]

(a)

GRAY SCALE DATA	Ds						
	Sf0	Sf1	Sf2	Sf3	Sf4	Sf5	Sf6
(000000)	H	L	L	L	L	L	L
(000001)	H	H	L	L	L	L	L
(000010)	H	L	H	L	L	L	L
(000011)	H	H	H	L	L	L	L
:	:	:	:	:	:	:	:
(111101)	H	H	L	H	H	H	H
(111110)	H	L	H	H	H	H	H
(111111)	H	H	H	H	H	H	H

(b) WHEN FR = L

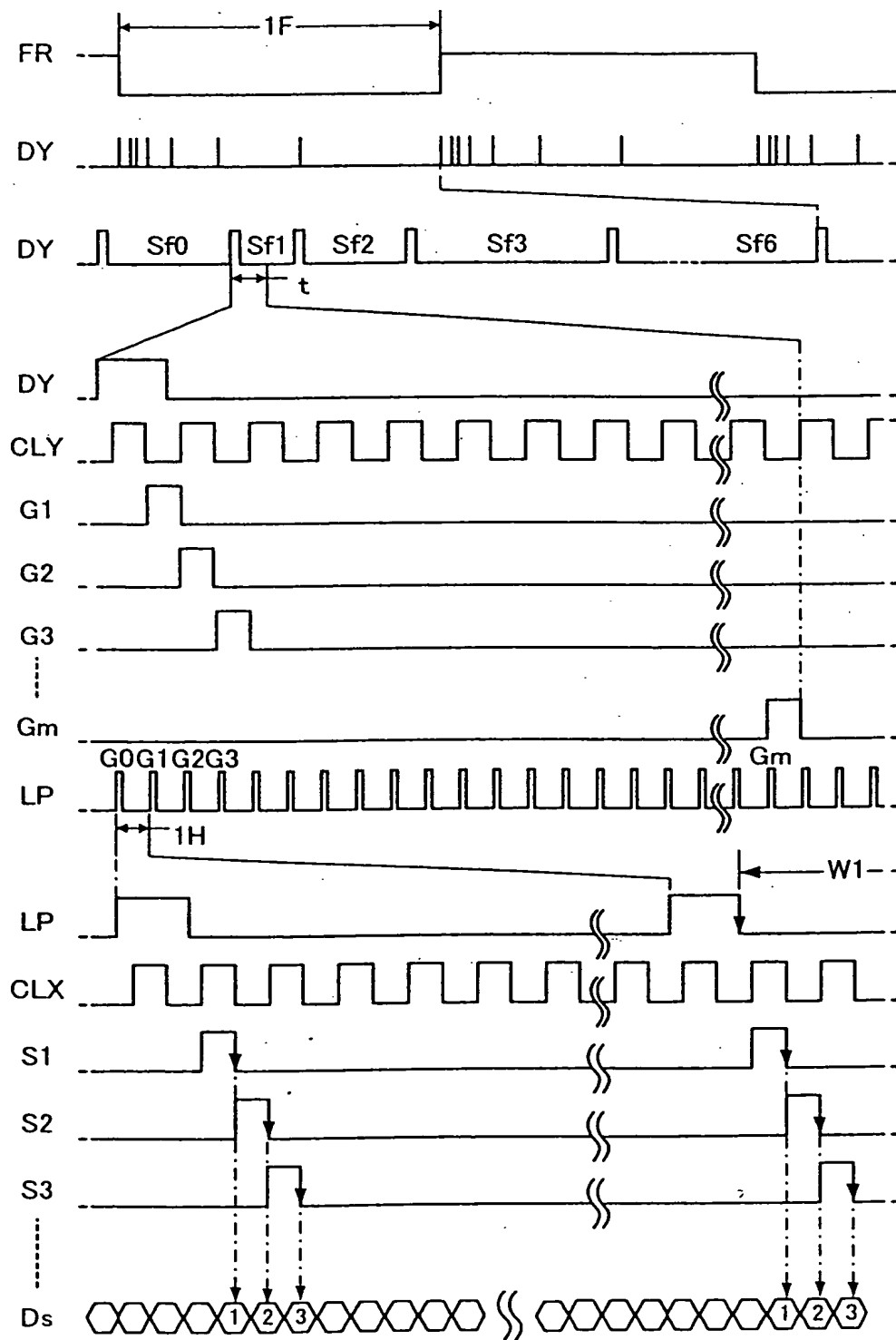
Ds	VOLTAGE
H	V1
L	0

(c) WHEN FR = H

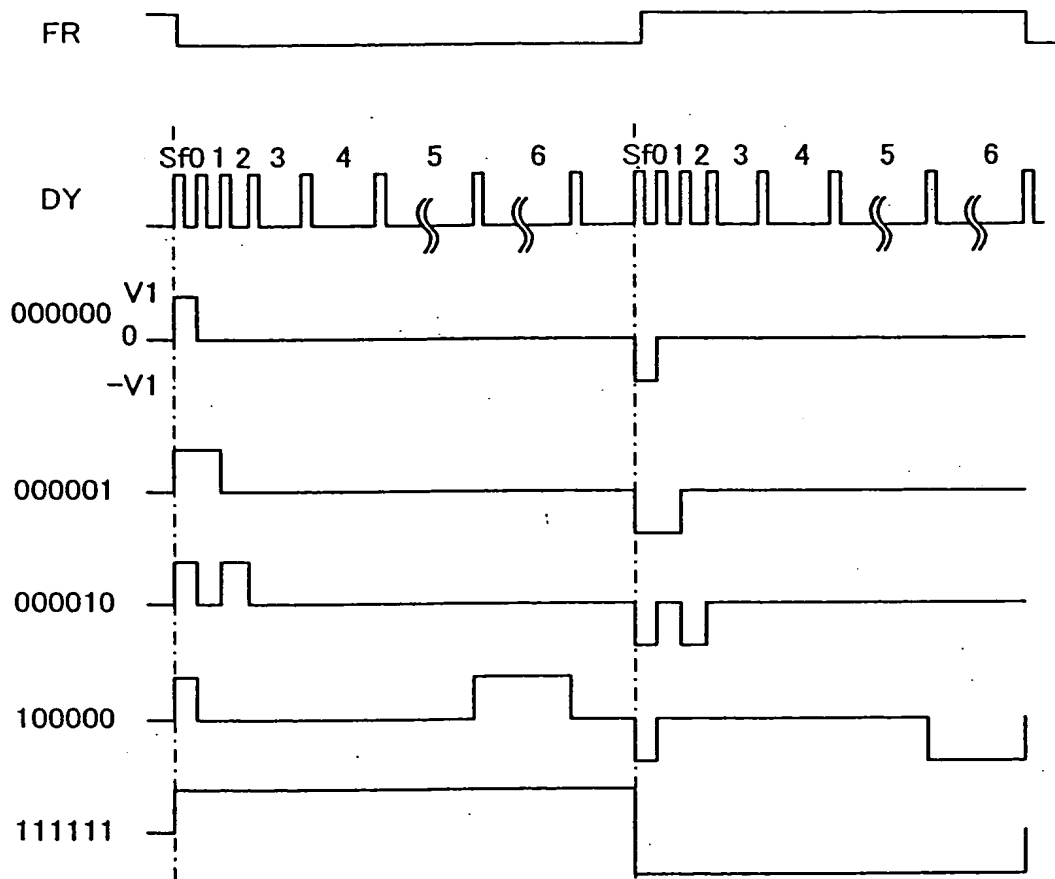
Ds	VOLTAGE
H	-V1
L	0

FIG. 7 is a truth table for the circuit of FIG. 6.

[FIG. 8]

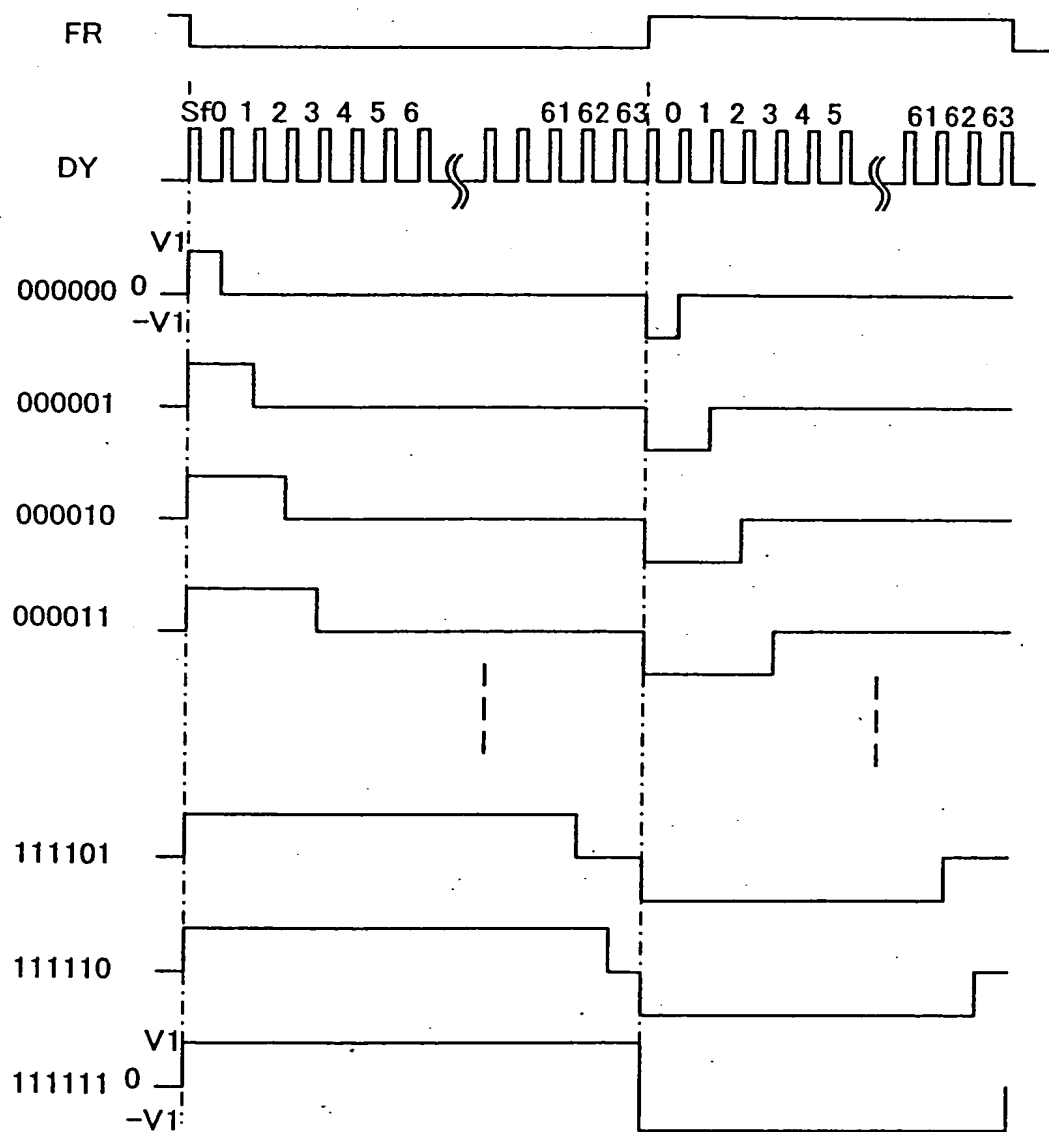


[FIG. 9]



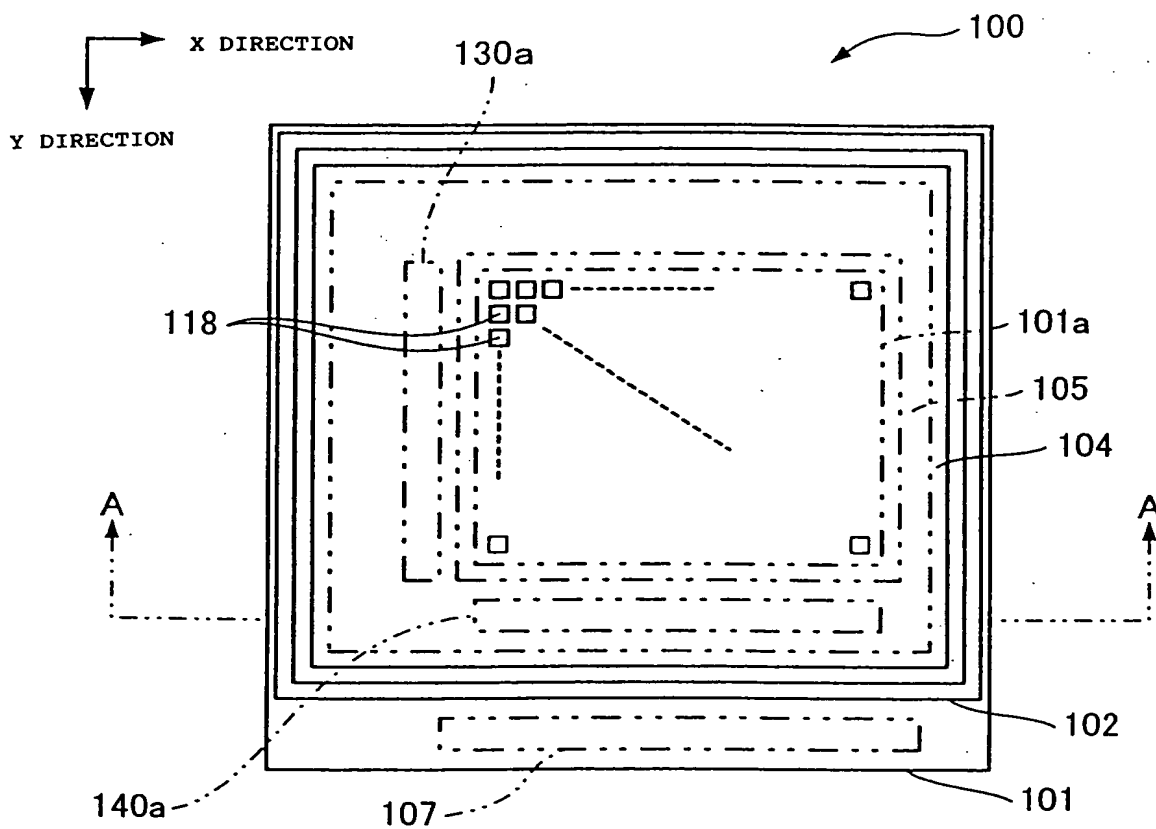
10-050-624450

[FIG. 10]

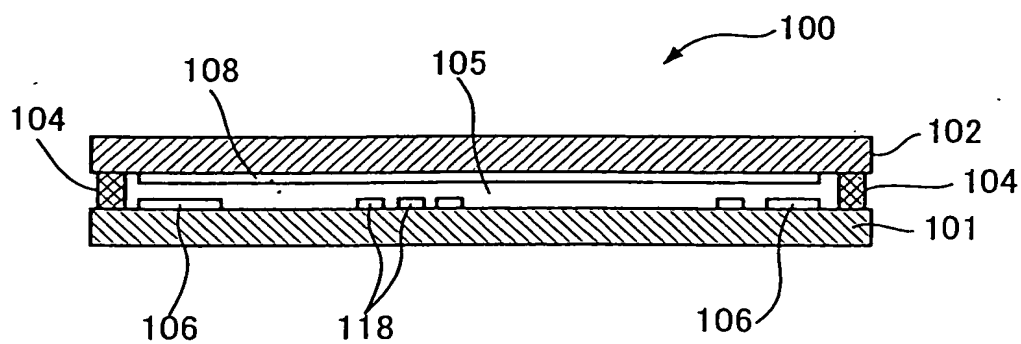


[FIG. 11]

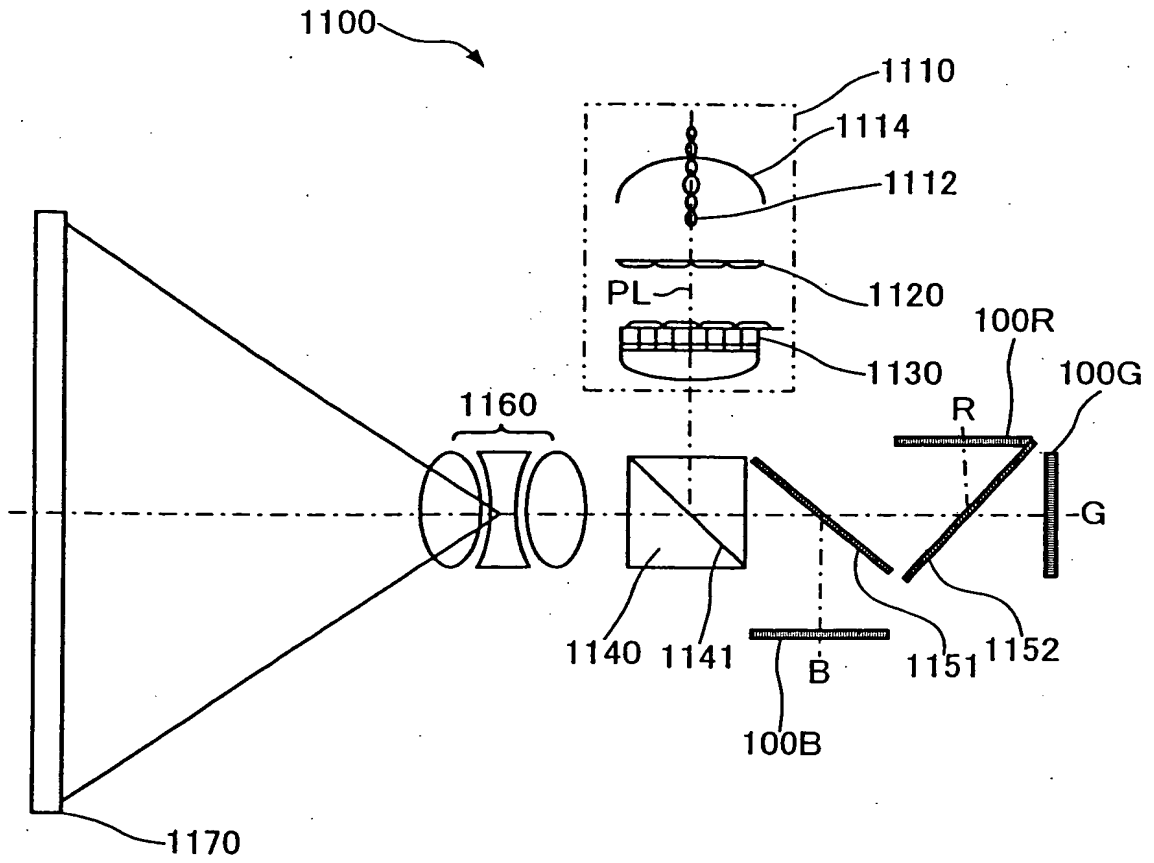
(a)



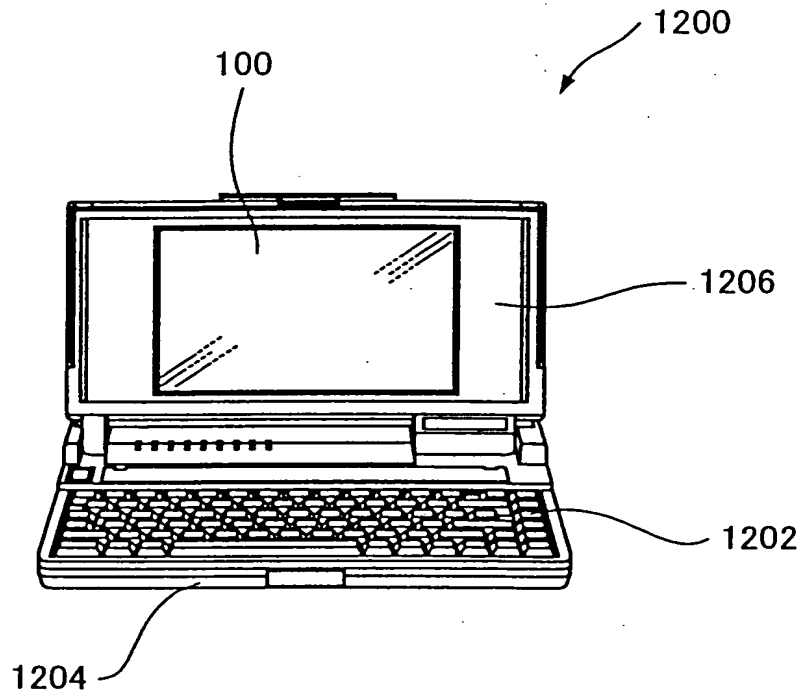
(b)



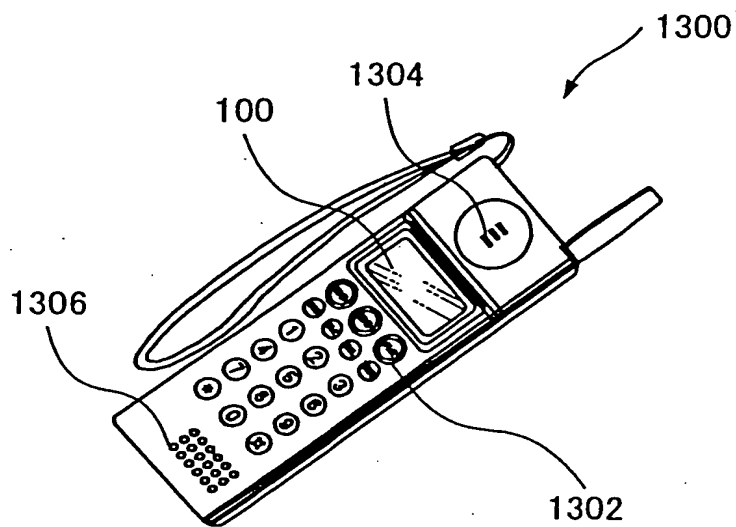
[FIG. 12]



[FIG. 13]



[FIG. 14]



[FIG. 15]

